

Fig. 1

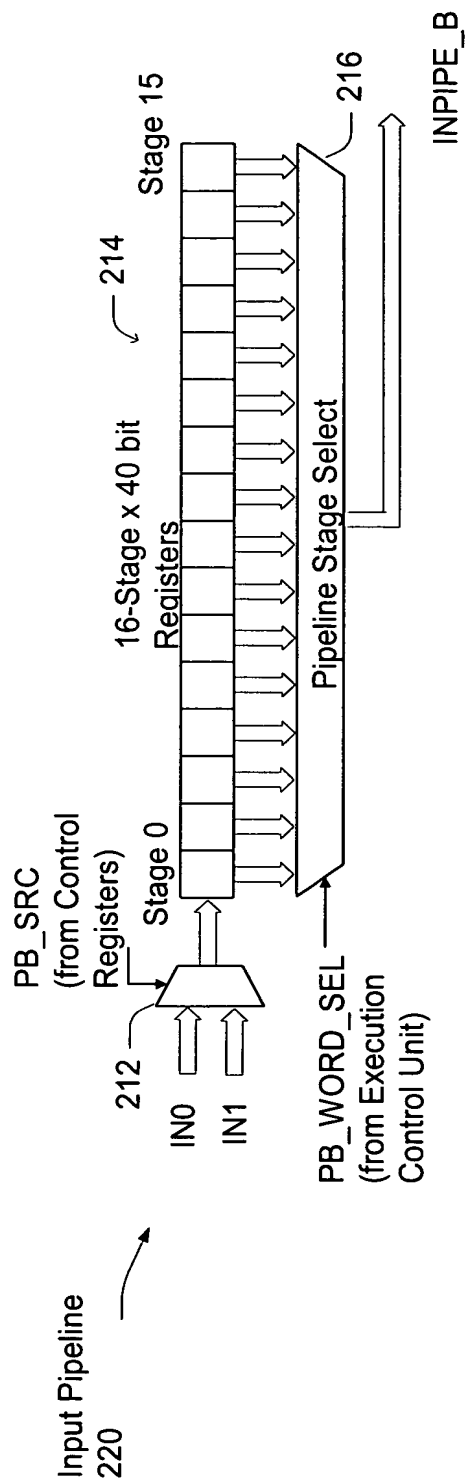
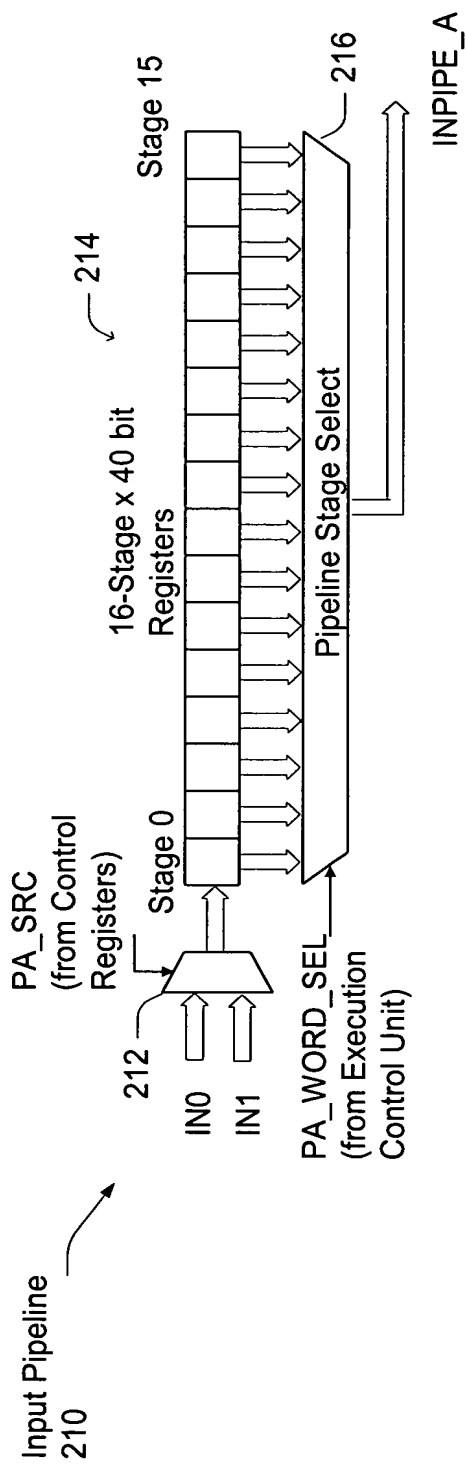


Fig. 2A

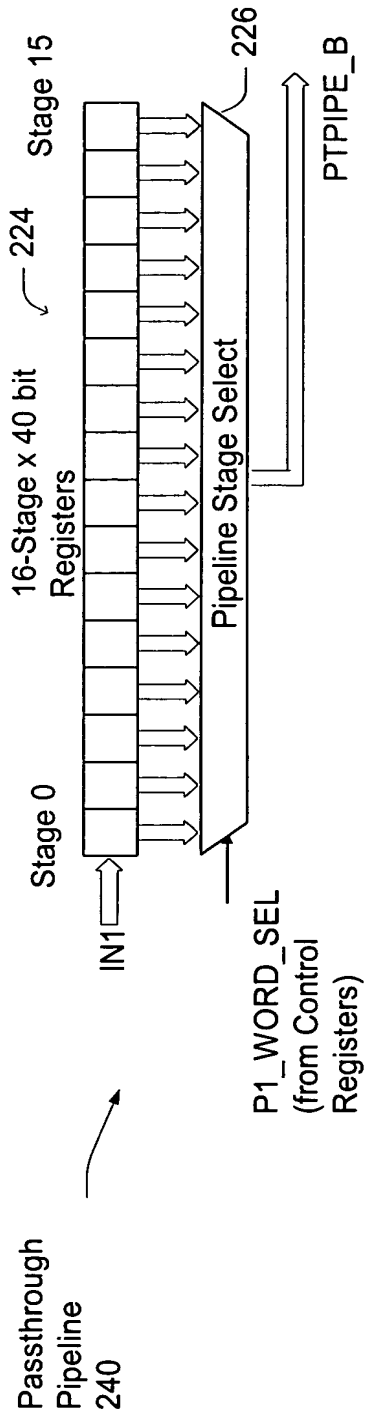
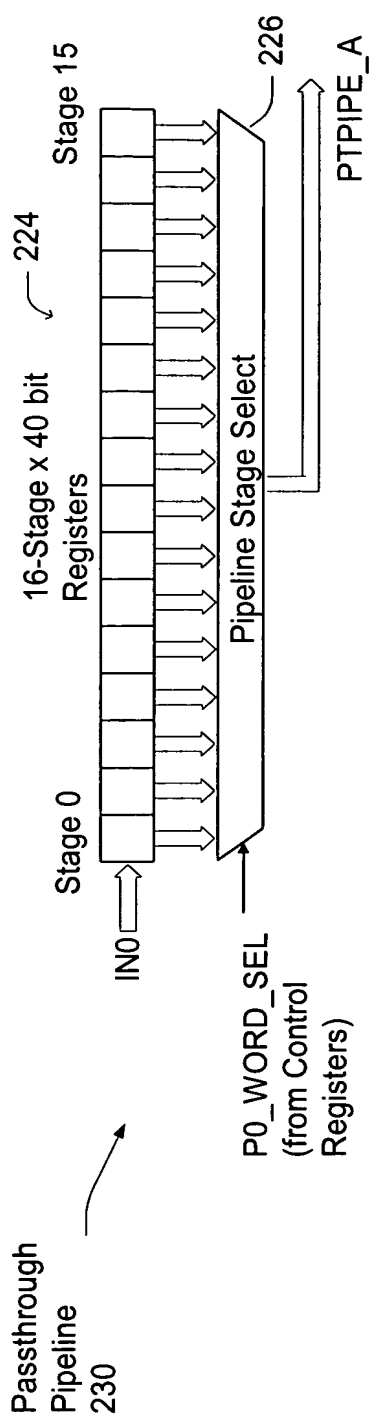


Fig. 2B

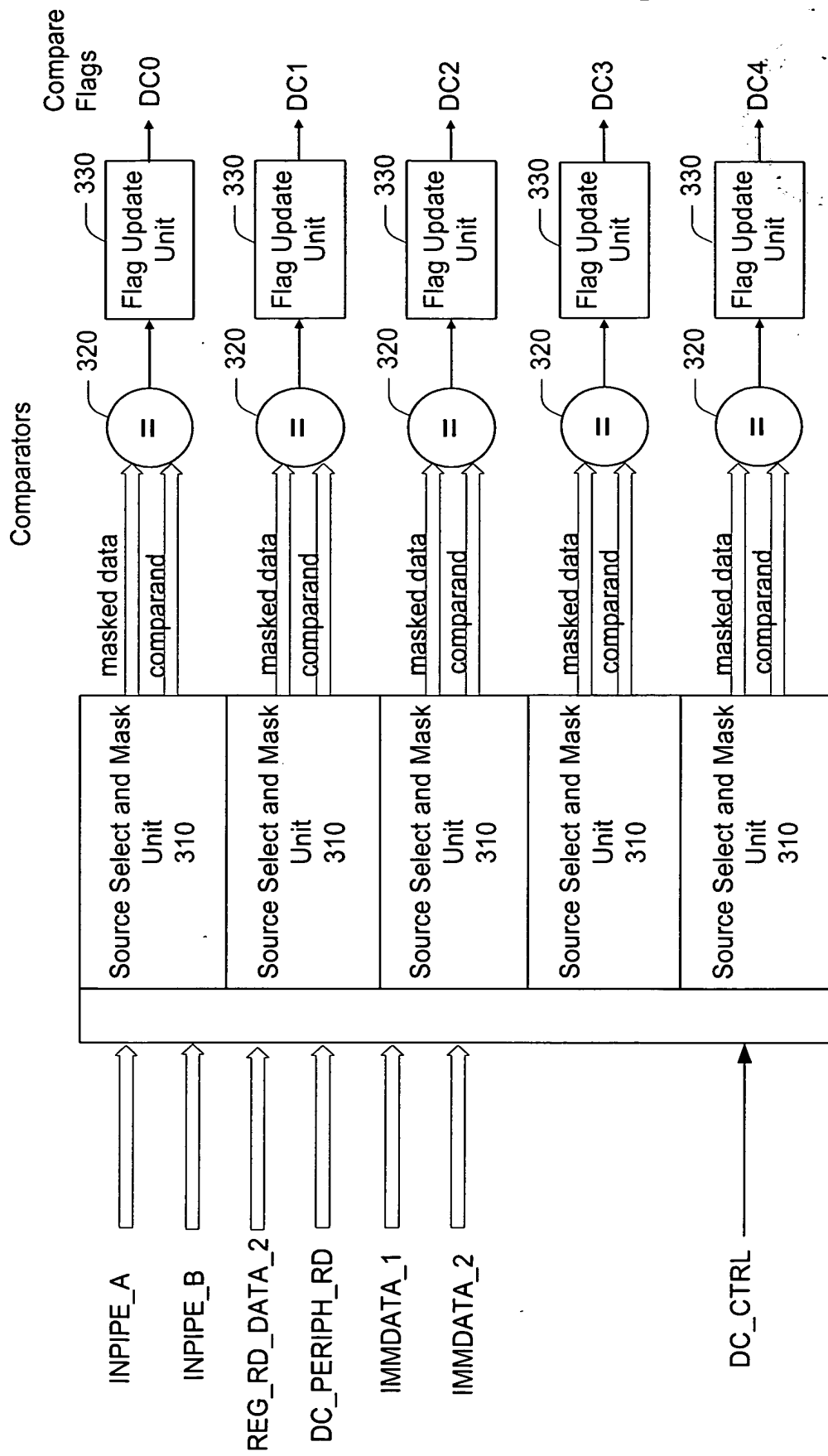


Fig. 3A

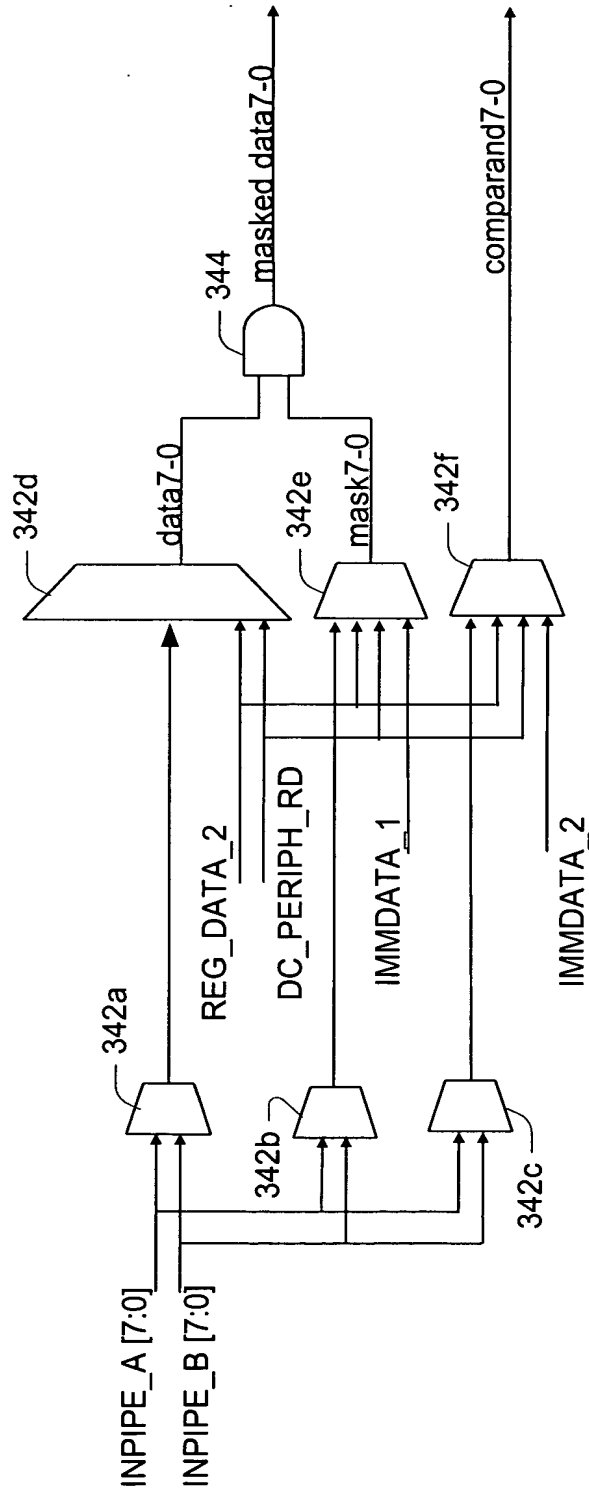


Fig. 3B

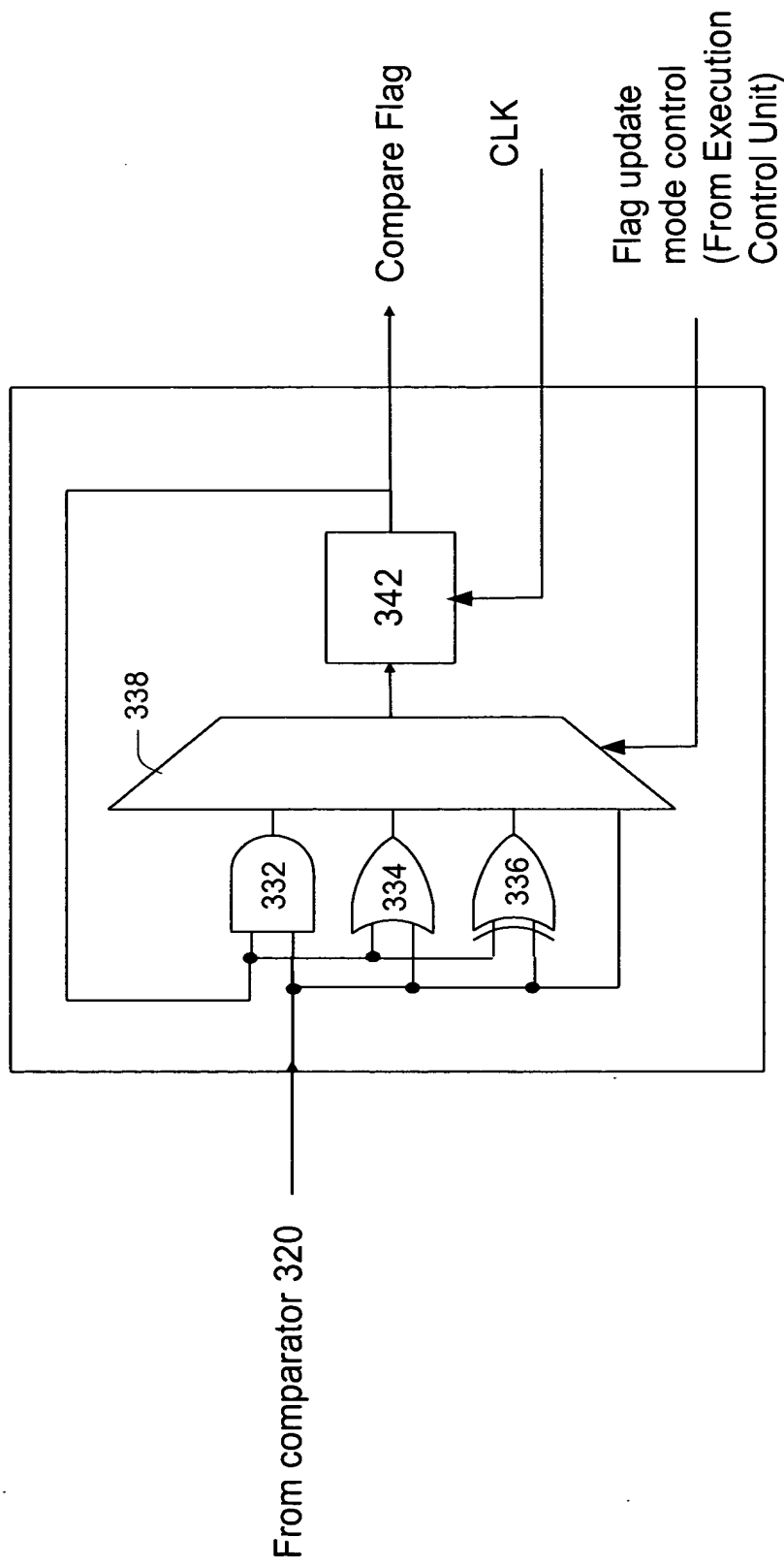


Fig. 3C

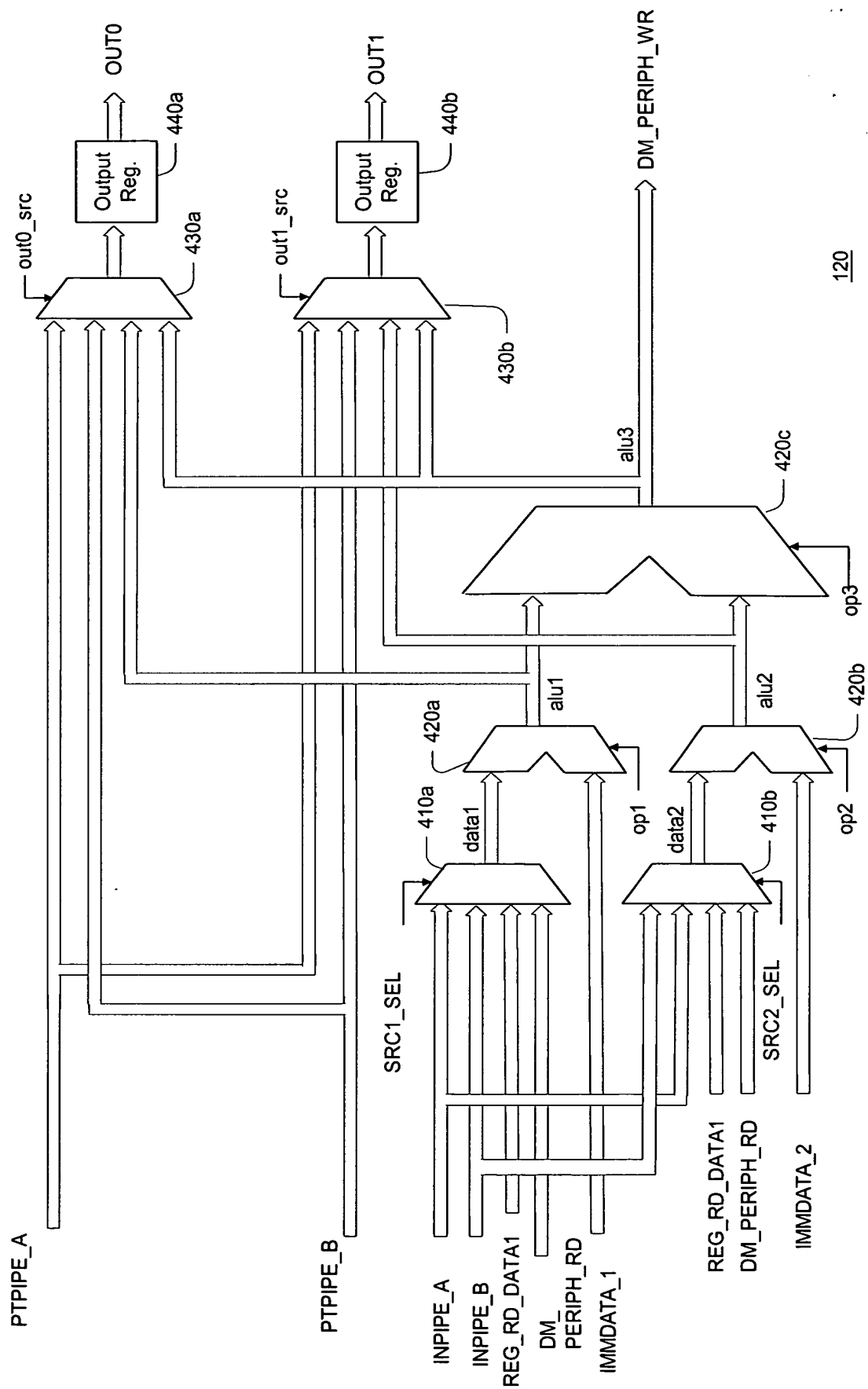


Fig. 4

FIG. 5 is a block diagram of a system 500 for jamming a device 512. The system 500 includes a first device 510, a second device 512, and a jamming system 520. The jamming system 520 includes two trace memory units 522, two jamming units 524, and a trigger unit 526. The first device 510 is connected to the jamming system 520 via a signal line 514 and a control line 516. The second device 512 is connected to the jamming system 520 via a signal line 514 and a control line 516. The jamming system 520 is configured to receive signals from the first device 510 and the second device 512, process them, and then jam the signals. The trigger unit 526 is configured to receive signals from the trace memory units 522 and control the jamming units 524. The jamming units 524 are configured to jam the signals received from the first device 510 and the second device 512. The trace memory units 522 are configured to store signals received from the first device 510 and the second device 512. The signal lines 514 and control lines 516 are used to transmit signals and control signals between the devices and the jamming system.

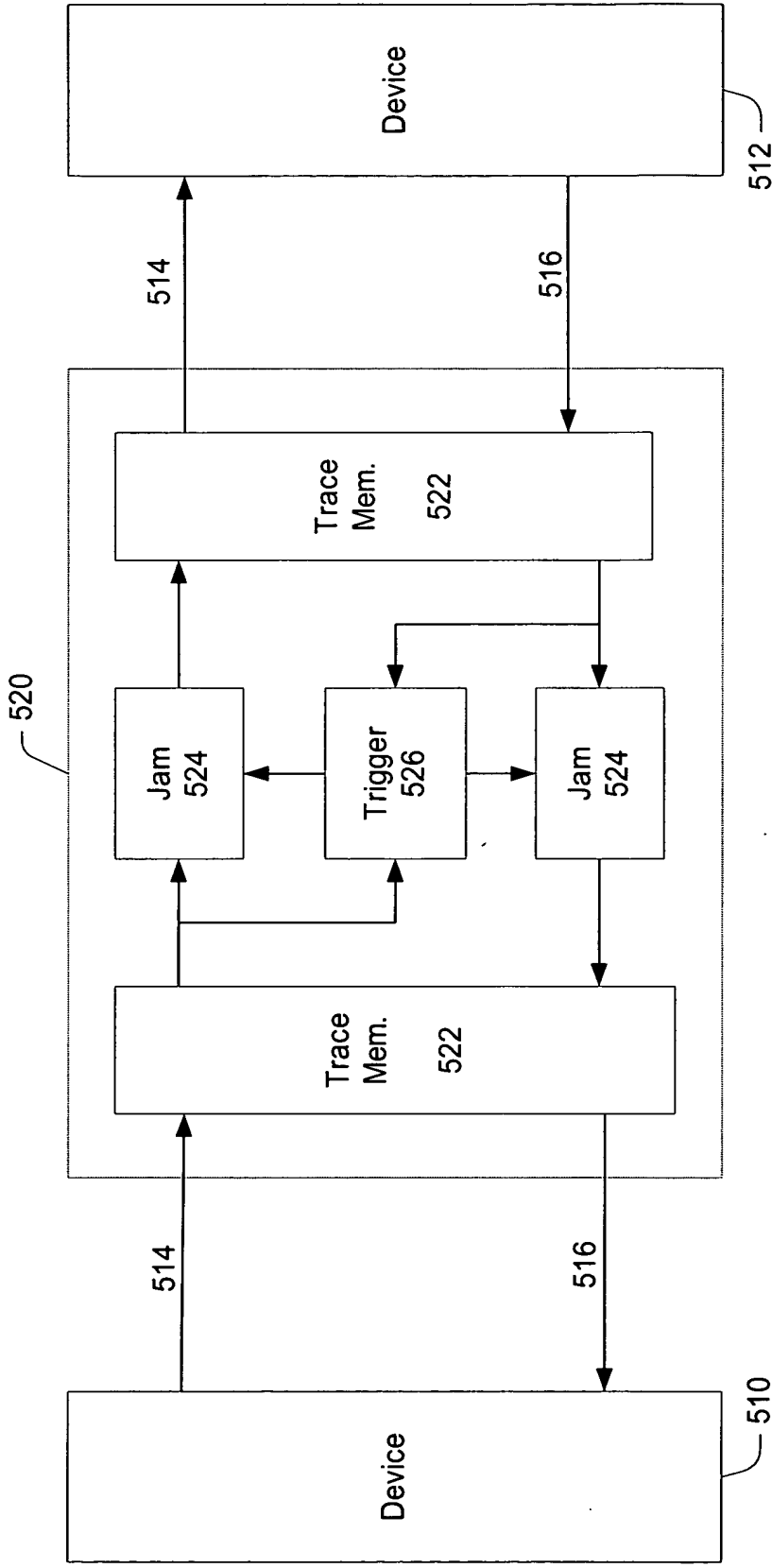


Fig. 5



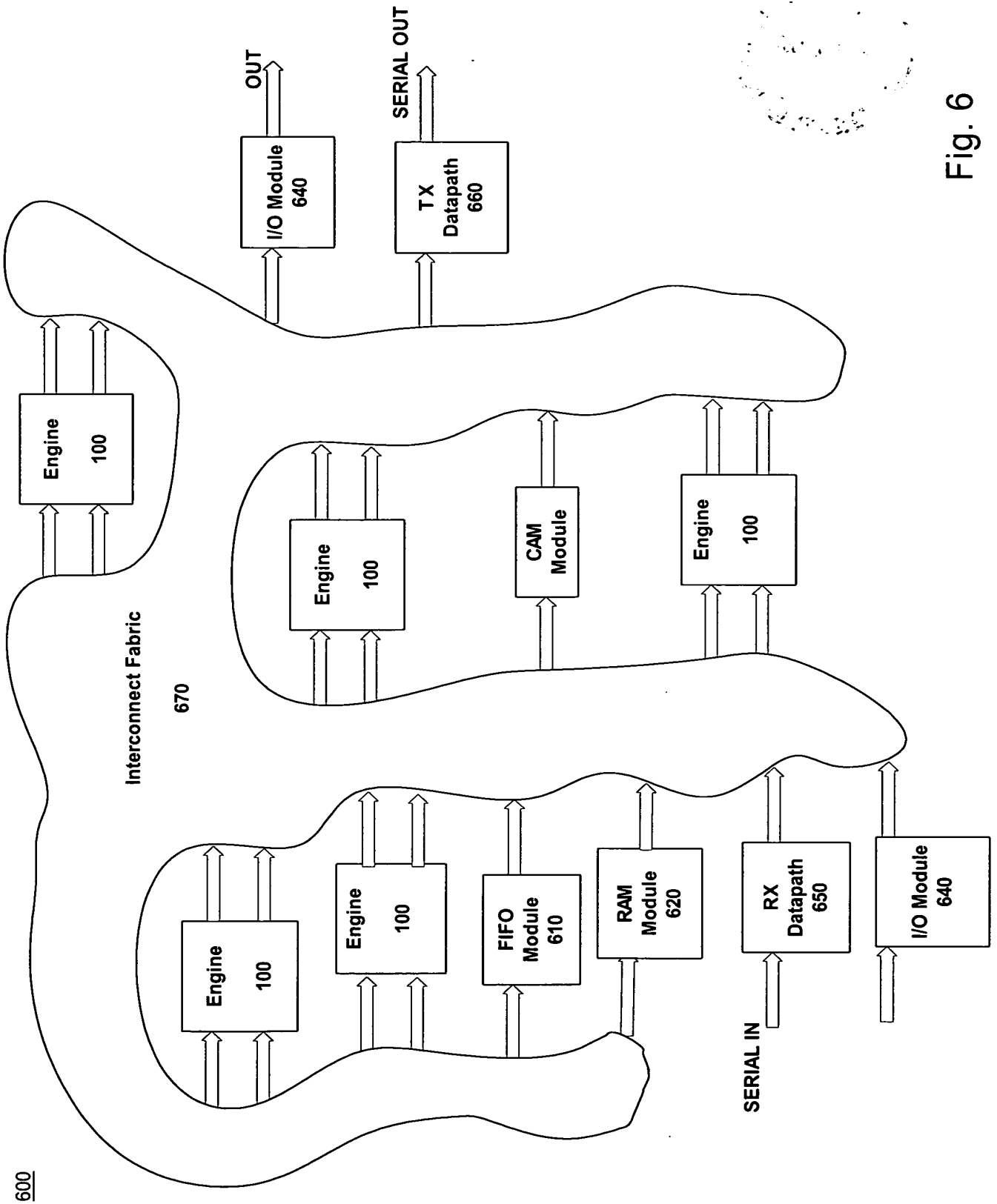


Fig. 6